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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re	U.S. Patent Application of)				
KON	DOH et al.)))))))))))				
Appli	Application Number: To Be Assigned					
Filed	: Concurrently Herewith)				
For:	PROGRAM COUNTER (PC) RELATIVE)				

Honorable Assistant Commissioner for Patents
Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT

Sir:

Pursuant to 37 C.F.R. §§ 1.56 and 1.97, this Information Disclosure Statement is submitted in the above-identified patent application. A listing of documents to be published on the face of any patent granted from this application is submitted herewith on Form PTO-1449. Any other documents or information submitted for consideration by the Examiner are listed in this paper. A copy of each U.S. and foreign patent, or each publication or portion thereof listed or herein identified, is submitted herewith.

This Information Disclosure Statement is submitted with the initial application. Accordingly, no fee is due or payable at this time.

The Examiner is requested to acknowledge consideration of the information provided in this paper in accordance with prescribed procedures.

Please charge any additional fees or credit any overpayments in connection with this paper to Deposit Account No. 08-1480.

Respectfully submitted,

Stanley P. Fisher

Registration Number 24,344

REED SMITH HAZEL & THOMAS LLP

3110 Fairview Park Drive Suite 1400 Falls Church, Virginia 22042 (703) 641-4200

December 18, 2001

Form PTO 1449 U.S. Department of Commerce				ATTY. DOCKET NUMBER HITA.0142 APPLICANT SERIAL NUMBER TO Be Assigned					7138		
Patent and Trademark Office . Information Disclosure Statement by Applicant				NDOH et of DATE ocurrently		· GROUP	GROUP				
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	Ot	her Docur	ments	(Including	Author, Title, Date	Pertinent Pa	iges, Etc.)			
		Other Documents (Including Author, Title, Date Pertinent Pages, Etc.) "SuperH™ RISC Engine SH-4, Programming Manual, Hitachi, Ltd. April 19, 2001, pp. 199, 10-22									
	-	Linley Gwennap, "MAJC Gives VLIW a New Twist", Microdesign Resources, September 13, 1999; Microprocessor Report, pp. 12-15, 22									
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